

Abstracts

A CMOS transmit/receive IF chip-set for WCDMA mobiles

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A transmit/receive IF chip-set integrating modulator/demodulator and AGCs for WCDMA mobiles in 0.35 /spl mu/m CMOS process is presented. Circuit innovations that achieve linear-in-dB gain control for wide-dynamic range AGCs with compensation against temperature and process variations to meet stringent specifications overcoming the limitations of CMOS technology are reported.

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